

REMARKS

Claims 1-23 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding objections and rejections are respectfully requested in light of the above amendments and following remarks.

Claim 1 was objected to because of informalities.

By the instant amendment, Claim 1 has been amended to eliminate the informalities.

It is respectfully submitted that Claims 1-23 are free from informalities.

Turning now to the art rejections, Claims 1-5, 7, 8, 10-16, 21, and 22 were rejected under 35 U.S.C. § 102(e) as being anticipated by Chung; Claims 6, 9, 7-20, and 23 were rejected under 35 U.S.C. § 103 as being unpatentable over Chung.

These rejections are respectfully traversed.

It is respectfully submitted that Chung does not disclose or suggest the presently claimed invention including the transconductance feedback block coupled directly between the resistive sensor and the serially coupled resistors and transistors in independent Claim 1, albeit defined as the transconductance feedback block directly coupled between the resistive sensor and the transistors in independent Claim 10.

Chung discloses a voltage source in between the R_{GMR} element 502 and element 542.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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